Computer-Aided Formal Verification (MT 2009) Binary Decision Diagrams (BDDs)

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Why?

Function Representations using Decision Diagrams

Reduced Ordered BDDs

Ordering and BDD Size

Data Structures for BDDs

Operations on BDDs

Motivation



The state space of interesting systems is too big, explicit enumeration will fail inevitably.

 However, the reachable state space isn't random, but follows specific rules

 We hope to exploit that with data structures that are concise in relevant cases

Binary Decision Diagrams (BDDs)





Randal E. Bryant

- Binary Decision Diagrams (BDDs) are a symbolic representation of Boolean functions
- Key idea: specific forms of BDDs are canonical
- [Bryant86] is one of the most-cited papers in computer science

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canonical, a. (and n.)

- 1. Prescribed by, in conformity with, or having reference to ecclesiastical edict or canon law.
- b. canonical hours: (a) stated times of the day appointed by the canons for prayer and devotion; (b) the hours (now from 8 a.m. to 3 p.m.) within which marriage can be legally performed in a parish church in England; (c) transf.
- c. canonical dress, etc.; the articles of dress worn by clergy according to canon.
- d. canonical obedience: the obedience to be rendered by inferior clergy to the bishop or other ecclesiastical superior, according to the canons.
- 2. Of or belonging to the canon of Scripture. (Also used of other sacred books.)
- canonical epistles, more particularly, the seven catholic epistles of James, Peter, John, and Jude; also applied to certain epistles of St. Basil, etc. Also quasi-n., *a canonical* (obs.) = CANON n.² 5.
- gen. Of the nature of a canon or rule; of admitted authority, excellence, or supremacy; authoritative; orthodox, accepted; standard.
- 5. Math. Furnishing, or according to, a general rule or formula (see CANON $n.^2$ 3).
- 6. Mus. According to the rules of canon, in canon form.
- 7. Of or belonging to an ecclesiastical chapter, or to one of its members (see CANON n.²).

Oxford English Dictionary



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Oxford English Dictionary

Explicit vs. Symbolic Representations of Functions



x	y	z	$\int f(x,y,z)$
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	1

 $f(x,y,z) = x \leftrightarrow (y \wedge z)$

Explicit Obviously gets large Symbolic Potentially smaller

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Decision Diagrams





- Distinguishes terminal from non-terminal nodes
- The edges are labeled with decisions
- The sink nodes (squares) are labeled with the outcome

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Decision Diagrams for Functions





- Inner nodes: a variable v
- Out-edges: value for v
- Terminal nodes: function value

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- ▶ represents Boolean function $f: \{0,1\}^n \to \{0,1\}$ (*n* = number of variables)
- as directed acyclic graph (DAG)
 - one root, two leaves (0 and 1), two (colored) edges at inner nodes
- as finite automaton (but no cycles)
 - ► accepts subset of {0,1}ⁿ, 1 as accepting state, 0 as dead-end state
- as branching program (but loop-free)
 - > <label>: if <var> goto <label> else goto <label>
 - > <label>: return [0 | 1]

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Drawing BDDs





A solid edge ("high edge") means the variable is 1

A dashed edge ("low edge") means the variable is 0

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Ordered BDDs







- Assign arbitrary total ordering to variables, e.g., x < y < z.
- Variables must appear in that order along all paths.

Ordered BDDs







- Assign arbitrary total ordering to variables, e.g., x < y < z.
- Variables must appear in that order along all paths.



A reduced ordered BDD (RO-BDD) is obtained from an ordered BDD by applying three rules:

- #1: Merge equivalent leaf nodes
- #2: Merge isomorphic nodes
- #3: Eliminate redundant tests

Reduction #1: Equivalent Leaf Nodes

This one is trivial:



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Reduction #2: Isomorphic Nodes





Merge nodes with the same variable and the same children

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Reduction #3: Redundant Tests



Eliminate nodes where both out-edges go into the same node:



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Canonicity of BDDs



- Reduced Ordered BDDs (RO-BDDs) [Bryant86]
 - Apply (algebraic) reduction rule until convergence
 - Nodes are ordered with respect to fixed variable order (ordered)
- We restrict our discussion to RO-BDDs (and just call them BDDs)

Theorem Fix variable order, then (RO)BDD for Boolean function *f* is unique Proof Obtained from unique minimal automata theorem

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Observation: given a BDD it takes constant time to check whether a formula is

- a tautology,
- inconsistent,
- satisfiable

Aren't these hard?

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Variable Ordering and BDD Size



Equality of two 2-bit vectors



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Variable Ordering and BDD Size



Equality of two 2-bit vectors

$$(a,c)=(b,d)$$
 equivalent to $(a\leftrightarrow b)\wedge(c\leftrightarrow d)$



unique BDD for (one) worst case, **blocked** variable ordering (exponential in the width of the bit vectors)

Selecting a Good Ordering



X Intractable problem

 Even when input is an OBDD (i.e., to find optimum improvement to current ordering)

Application-based heuristics

- Exploit characteristics of application
- E.g., ordering for functions of combinational circuit: traverse circuit graph depth-first from outputs to inputs

Data Structures for BDDs



Nodes are numbered 0, 1, 2, 3, ...
 (0, 1 are the terminals)

 \blacktriangleright Variables are numbered $1,2,3,\ldots,n$

Data Structures for BDDs



Node table: $T: u \rightarrow (i, l, h)$

Operations:

- ▶ init(*T*)
- ▶ u := add(T, i, l, h)
- var(u)
- ► low(*u*)
- ► high(u)

Inverse of node table: $H:(i,l,h) \rightarrow u$

Operations:

- ► init(H)
- ▶ u := lookup(H, i, l, h)

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• insert(H, i, l, h, u)

Node Table



Initial State:

#	var	low	high
0	n+1		
1	n+1		

(The n + 1 variable number for 0/1 will become clear later).

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Node Table: Example





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Inserting a Node into T



```
MK[T, H](i, l, h)
     if l = h then
           return l;
     else if lookup(H, i, l, h) \neq then
           return lookup(H, i, l, h);
     else
           u := \operatorname{add}(T, i, l, h);
           insert(H, i, l, h, u);
           return u:
```

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Goal: build BDD for f.

Use Shannon's expansion as follows

$$f = (\neg x \land f|_{x=0}) \lor (x \land f|_{x=1})$$

to break problem into two subproblems. Solve subproblems recursively.

Building a BDD



$\begin{array}{l} \mathsf{BUILD}[T,H](f) \\ \textbf{return } \mathsf{BUILD2}(f,\,1); \end{array}$

```
function BUILD2(f, var) =
```

```
if var > n then
```

if f is false then return 0 else return 1;

else

```
f_0 := \mathsf{BUILD2}(f[0/x_i], var + 1);
f_1 := \mathsf{BUILD2}(f[1/x_i], var + 1);
return MK[T,H](var, f_0, f_1);
```

end

Basic Operations on BDDs



Canonicity implies

- $f \equiv g$ iff. BDDs for f and g are equal
- ► *f* tautology iff. BDD for *f* is 1
- ► f satisfiable iff. BDD for f is not 0

Basic operation: RESTRICT

- $f|_{x=0}$: replace all x nodes by their low-edge sub-tree.
- ► f|_{x=1}: replace all x nodes by their high-edge sub-tree.



Conjunction, ...: $f(a, b, c, d) \star g(a, b, c, d)$, where the symbol \star denotes some binary operator.

Again, use Shannon's expansion as follows

$$f \star g = \neg x \wedge (f|_{x=0} \star g|_{x=0}) \lor x \wedge (f|_{x=1} \star g|_{x=1})$$

to break problem into two subproblems. Solve subproblems recursively.

Applying a Function



function APPLY $(u_1, u_2) =$ if $u_1, u_2 \in \{0, 1\}$ then $u := u_1 \star u_2$ else if $var(u_1) = var(u_2)$ then $u := \mathsf{MK}(\mathsf{var}(u_1), \mathsf{APPLY}(\mathsf{low}(u_1), \mathsf{low}(u_2))),$ APPLY(high (u_1) , high (u_2))): else if $var(u_1) < var(u_2)$ then $u := MK(var(u_1), APP(low(u_1), u_2), APP(high(u_1), u_2));$ **else** (* $var(u_1) > var(u_2)$ *) $u := \mathsf{MK}(\mathsf{var}(u_2), \mathsf{APP}(u_1, \mathsf{low}(u_2)), \mathsf{APP}(u_1, \mathsf{high}(u_2)));$ return u;

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Example





 x_1



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Example





Now compute BDD for $(x_1 \iff x_2) \lor \neg x_2$ using APPLY!

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Apply(5, 4): var(5)=1, var(4)=2



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APPLY(5, 4): var(5)=1, var(4)=2 MK(var(5), APP(low(5),4), APP(high(5),4));



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APPLY(5, 4): var(5)=1, var(4)=2 MK(var(5), APP(low(5),4), APP(high(5),4)); MK(1, APP(4,4), APP(3,4));



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MK(2, APP(0,1), APP(1,0));

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APPLY(5, 4): var(5)=1, var(4)=2MK(var(5), APP(low(5),4), APP(high(5),4));MK(1, APP(4,4), APP(3,4)); MK(1, 4, 1); **APP**(4, 4): var(4)=var(4)=2MK(2, APP(1,1), APP(0,0)); MK(2, 1, 0); = 4!**APP**(3, 4): var(3)=var(4)=2MK(2, APP(0,1), APP(1,0)); MK(2, 1, 1): =1



Final result:



#	var	low	high
0	3		
1	3		
2	-	_	_
3	-	—	—
4	$2x_2$	1	0
5	—	—	_
6	$1 x_1$	4	1

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Existential Quantification



For BDD f and a variable x compute BDD for

 $\exists x. f$

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Existential Quantification



For BDD f and a variable x compute BDD for

 $\exists x. f$

This is equivalent to

$$f|_{x=0} \lor f|_{x=1}$$

- Two RESTRICT operations
- One call to APPLY

Summary: Binary Decision Diagrams (BDDs)



Canonical representation of boolean functions

- Every Boolean function has a unique RO-BDD
- Try to obtain small graphs by means of sharing

 Efficient manipulation algorithms (e.g., conjunction, quantification, ...)

X Often explode in size